

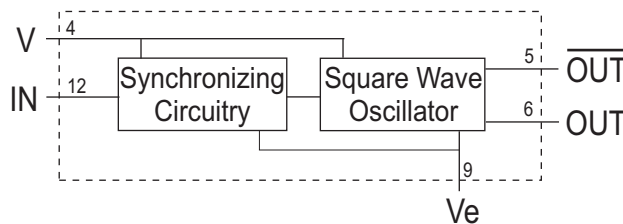
100K ECL Digital Frequency Multiplier Module

The 100K ECL Digital Frequency Multiplier Modules manufactured by Engineered Components Company are designed to provide a square wave output at a given frequency which can be synchronized by square wave inputs at sub-harmonic frequencies. The falling edge of the output waveform is synchronized to the falling edge of the input waveform. If no synchronizing input is present, the unit will free-run producing a continuous output square wave. Phase jitter at the output will increase as higher orders of multiplication are used. An inverted output is also supplied.

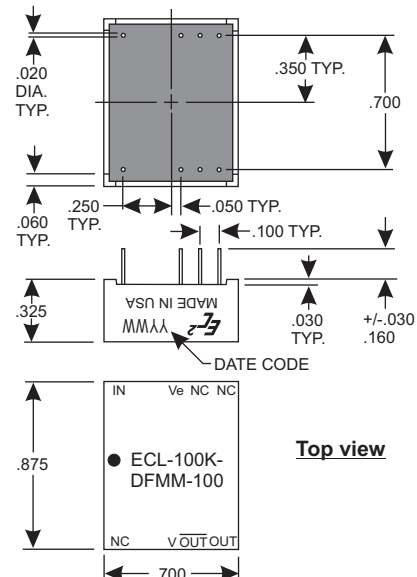
The MTBF on these modules, when calculated per MIL-HDBK-217, for a 50 deg.C ground fixed environment and with 50VDC applied, is in excess of 1.9 million hours. The temperature coefficient of delay is less than +/-300 ppm/deg.C over the operating temperature range of 0 to +85 deg. C.

The module is provided in a 12-pin DIP package, fully encapsulated in epoxy resin and is housed in a Diallyl Phthalate case, blue in color. The case marking is applied by silkscreen using white epoxy paint. The 8 copper leads are tin-lead plated and meet the solderability requirements of MIL-STD-202, Method 208.

BLOCK DIAGRAM



MECHANICAL DIAGRAM



Product Selection Table

Part Number	Nominal Output Frequency	Output Frequency Tolerance
ECL-100K-DFMM-50	50 MHz	+/-2%
ECL-100K-DFMM-55	55 MHz	+/-2%
ECL-100K-DFMM-60	60 MHz	+/-2%
ECL-100K-DFMM-65	65 MHz	+/-2%
ECL-100K-DFMM-70	70 MHz	+/-2%
ECL-100K-DFMM-75	75 MHz	+/-2%
ECL-100K-DFMM-80	80 MHz	+/-2%
ECL-100K-DFMM-85	85 MHz	+/-2%
ECL-100K-DFMM-90	90 MHz	+/-2%
ECL-100K-DFMM-95	95 MHz	+/-2%
ECL-100K-DFMM-100	100 MHz	+/-2%
ECL-100K-DFMM-110	110 MHz	+/-2%
ECL-100K-DFMM-120	120 MHz	+/-2%
ECL-100K-DFMM-130	130 MHz	+/-2%
ECL-100K-DFMM-140	140 MHz	+/-2%
ECL-100K-DFMM-150	150 MHz	+/-2%
ECL-100K-DFMM-160	160 MHz	+/-2%
ECL-100K-DFMM-170	170 MHz	+/-2%
ECL-100K-DFMM-180	180 MHz	+/-2%
ECL-100K-DFMM-190	190 MHz	+/-2%
ECL-100K-DFMM-200	200 MHz	+/-2%
ECL-100K-DFMM-210	210 MHz	+/-2%
ECL-100K-DFMM-220	220 MHz	+/-2%
ECL-100K-DFMM-230	230 MHz	+/-2%
ECL-100K-DFMM-240	240 MHz	+/-2%
ECL-100K-DFMM-250	250 MHz	+/-2%

Special modules can often be manufactured to provide for customer specific applications.

Operating Specifications:

All measurements made at 25 deg. C
 All measurements made with $V_e = -4.5\text{VDC}$, $V_c = 0\text{VDC}$
 All measurements made with (1) 100K ECL output load
 All measurements made with a 50 ohm pulldown resistor to -2VDC at the input and output

Operating Temperature: 0 to +85 deg. C
 Storage Temperature: -55 to +125 deg. C

V_e Supply Voltage: -4.5 +/-5% VDC

V_{cc} Supply Current: 102mA typical

Logic "High" Input:

Voltage: -0.88VDC min.

Current: 700uA max.

Logic "Low" Input:

Voltage: -1.81VDC max.

Current: 1.0uA min.

Logic "High" Voltage Out: -1.025VDC min.

Logic "Low" Voltage Out: -1.625VDC max.



engineered components company

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